

REMARKS

Applicant is in receipt of the Office Action mailed September 27, 2004.

Rejections Under Section 102

Claim 15 was rejected under 35 U.S.C. 102(b) as being anticipated by Poole et al. U.S. Patent 5,798,767. Claim 15 is cancelled.

Rejections Under Section 103

Claims 1, 3-5, and 7-14 were rejected under 35 U.S.C. 103(a) as being unpatentable over Poole et al. U.S. Patent 5,798,767 (hereinafter referred to as Poole).

Claim 6 was rejected under 35 U.S.C. 103(a) as being unpatentable over Poole et al. U.S. Patent 5,798,767 in view of Buckley et al. U.S. Patent 6,104,836.

Claim 1 as herein amended recites the limitations:

“A circuit capable of performing a variety of graphics data transforming operations, said circuit comprising:

one or more groups of four identical, independent sub-circuits, wherein each sub-

circuit performs a specified operation on one of four pixel values

specifying color values and transparency, and wherein each sub-circuit is a

set of circuit elements comprising:

a subtractor coupled to receive a first input and a second input;

a first shift unit coupled to the output of the subtractor and configured to

implement a specified preshift to the output of the subtractor;

a multiplier coupled to a third input and an output of the first shift unit;

a second shift unit coupled to the output of the multiplier and configured

to implement a specified post-shift to the output of the multiplier;

an adder coupled to a fourth input and an output of the second shift unit;

a first multiplexor whose output drives the first input;
a second multiplexor whose output drives the second input;
a third multiplexor whose output drives the third input; and
a fourth multiplexor whose output drives the fourth input; and
an operation register, wherein a set of one or more values stored in the operation register specifies a specific graphics data transforming operation; and
control logic configured to drive selection lines of the first, second, third and fourth multiplexors in each sub-circuit in response to the set of one or more values in the operation register.”

Poole neither teaches nor suggests “one or more groups of four identical, independent sub-circuits, wherein each sub-circuit performs a specified operation on one of four pixel values specifying color values and transparency”. In fact, Poole teaches away from four identical, independent sub-circuits in Figure 7, which clearly shows three non-identical circuits for performing graphics data operations on red, green, and blue color values. In addition, only the circuit 152b in Figure 7, shows multiplexors coupled to the input of the multiplier and adder.

Furthermore, Poole neither teaches nor suggests “a first shift unit coupled to the output of the subtractor and configured to implement a specified preshift to the output of the subtractor” or “a second shift unit coupled to the output of the multiplier and configured to implement a specified post-shift to the output of the multiplier”.

Therefore, claim 1 as herein amended and its dependents are patentably distinguished over Poole for at least the reasons given above.

Claim 8 as herein amended contains limitations similar to claim 1. Therefore, claim 8 and its dependents are patentably distinguished over Poole for at least the reasons given above in support of claim 1.

CONCLUSION

In light of the foregoing amendments and remarks, Applicant submits the application is now in condition for allowance, and an early notice to that effect is requested.

If any extensions of time (under 37 C.F.R. § 1.136) are necessary to prevent the above referenced application(s) from becoming abandoned, Applicant(s) hereby petition for such extensions. If any fees are due, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert & Goetzel PC Deposit Account No. 50-1505/5681-13900/JCH.

Also enclosed herewith are the following items:

- ☒ Return Receipt Postcard
- ☐ Request for Approval of Drawing Changes
- ☐ Notice of Change of Address
- ☐ Check in the amount of \$ for fees ().
- ☐ Other:

Respectfully submitted,



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